Lab Report Format

Each student should submit her/his own report using the format provided below. You should provide a detailed description/discussion for each lab report item including all Verilog code and simulation waveforms. Note: some labs may require you to provide further information within your lab report.

1. Student Information
   Lab Number and Title:
   Name:
   E-mail:
   Partner’s Name (if applicable):
   Partner’s E-mail (if applicable):
   Lab Section:
   TA:

2. Lab Purpose
   Provide a brief summary of the lab assignment.

3. Implementation Details
   Provide a detailed description of the steps you followed to implement the lab. Be sure to include all relevant Verilog code.

4. Experimental Results
   Describe the results of the lab assignment. Be sure to include any simulation.

5. Significance
   What is the importance of the lab? How will you use the information you learned in this lab in the future? In which applications you can use this information?

6. Comments/Suggestions
   Please provide any comments or suggestions you may have for this lab assignment. Please indicate any areas of difficulty you encountered for this lab assignment.

7. Additional Lab Specific Information
   Attach any additional information requested for this lab, including schematics, simulation waveforms, or manually performed tasks.